## "VOLTAGE BOOSTER DEVICE AND MEMORY SYSTEM"

## Abstract

Voltage booster device (3) such as to selectively

assume an active status and a stand-by status, said

device comprising:

- a first terminal (15) such as to assume a respective electric potential and associated to a first capacitor (16),
- a second terminal (10) associated to a second capacitor (11) and selectively connectable to the first terminal (15),

characterised in that it also comprises circuital means (100) for discharging the first capacitor thus reducing in module the electrical potential of the first terminal (15), the circuital means being activated to functioning when said device in the stand-by status and the second terminal (10) is disconnected from said first terminal (15).

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